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Examiner Initials	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
EBK		Arnold, J. M., "An Architecture Simulator For National Semiconductor's Adaptive Processing Architecture (NAPA)," FPGAS For Custom Computing Machines, IEEE Comput. Soc., pp 271-72, April 15, 1998.	
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Application Number	09 / 521,280					
Filing Date	07-Mar-2000					
First Named Inventor	Stephan Voges					
Art Unit	2122					
Examiner Name	Eric B. Kiss					
Attorney Docket Number	EFIM0252					

			U.S. PAT	ENT DOCUMENTS	
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